

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

IKEMATSU et al.

Application No. Unassigned

Filed: October 22, 2003

Art Unit: Unassigned

Examiner: Unassigned

For: METHOD FOR MANUFACTURING
SEMICONDUCTOR DEVICE HAVING
GATE ELECTRODE

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Prior to the examination of the above-identified patent application, please enter the following amendments and consider the following remarks.